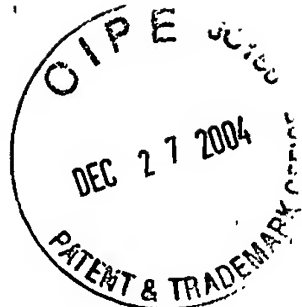


Customer No. 035884



JPW

In re application of:

Seung Hyun Yi, Kuk Tae Hong

Serial No: 10/688,607

Filed: October 17, 2003

For: OUTPUT DRIVING CIRCUIT

Art Unit: 2816

Examiner: Wells, Kenneth B.

PATENT

Attorney Docket: 2080-3-188

Mail Stop Non-Fee Amendments
Commissioner for Patents-
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an AMENDMENT in the above-identified application.

- ☐ Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
- ☐ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
- ☐ A petition for extension of time for ___ month(s) is enclosed.
- ☐ An information disclosure statement in accordance with 37 CFR 1.56 and 1.97 is enclosed.
- ☒ No additional fee is required.

The fee has been calculated as shown below:

| | (Col. 1) CLAIMS REMAINING AFTER AMENDMENT | | (Col. 2) HIGHEST NUMBER PREVIOUSLY PAID FOR | (Col. 3) PRESENT EXTRA* | LG/SM \$ ENTITY FEE | | ADD'L FEE DUE |
|---|---|---|---|-------------------------------|--|------|------------------|
| TOTAL CLAIMS FEE | 7 | - | 17 ** | 0 | LG=\$18 SM=\$9 | \$18 | \$ 0 |
| INDEPENDENT CLAIMS FEE | 1 | - | 1 *** | 0 | LG=\$84 SM=\$42 | \$84 | \$ 0 |
| FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIMS | | | | | LARGE ENTITY FEE = \$280 SMALL ENTITY FEE = \$140 | | \$ 0 |
| TOTAL | | | | | | | \$ 0 |

* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.

*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space. The "Highest Number Previously Paid For" (Total or Independent) is the highest number found from the equivalent box on Col. 1 of a prior amendment or the number of claims originally filed.

- ☐ A check in the amount of \$_____ to cover the filing fee is enclosed.
- ☐ A check in the amount of \$_____ to cover the extension fee is enclosed.
- ☐ A check in the amount of \$_____ to cover the information disclosure statement fee is enclosed.
- ☐ A check in the amount of \$_____ to cover the petition fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to **Deposit Account No. 502290**.
- ☒ Any filing fees under 37 CFR 1.16 for the presentation of extra claims.
- ☒ Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,
LEE, HONG, DEGERMAN, KANG & SCHMADEKA

Date: December 21, 2004

Customer No. 035884

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Lew Edward V. Macapagal
Registration No. 55,416
Attorney for Applicant(s)

Customer No. 035884

Attorney Docket No.: 2080-3-188

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Seung Hyun Yi, Kuk Tae Hong

Serial No: 10/688,607

Filed: October 17, 2003

For: OUTPUT DRIVING CIRCUIT

Art Unit: 2816

Examiner: Wells, Kenneth B.

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Introductory Comments:

This is in response to the Office Action dated September 21, 2004, in connection with the above-identified application. Claims 1-8 and 17 are pending in the current application pursuant to an election withdrawing claims 9-16 from consideration.

This application is amended by the virtue of this response, pursuant to 37 C.F.R. § 1.121. It is submitted that no new matter has been introduced with this amendment. The consideration of the amendments and remarks provided below are respectfully requested.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450, on
December 21, 2004
Date of Deposit
Lew Edward V. Macapagal
Name
Lew Edward V. Macapagal 12/21/2004
Signature Date

Amendments to the Title

Please amend the title as follows:

OUTPUT DRIVING CIRCUIT FOR MAINTAINING I/O SIGNAL DUTY RATIOS